Sizing Linear and PWM Amplifiers Driving a Linear Brushless Motor

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***Abstract*—This application note provides a design process for sizing linear and PWM three-phase amplifiers driving a linear brushless motor (LBM). Design inputs consist of the motor force constant, back-emf constant, pitch, winding resistance and inductance, the load mass, and the worst-case velocity and load force profiles. The design outputs are the five key amplifier requirements: amplifier bus voltage(s), peak output current, continuous output current, peak output power, and continuous power dissipation. The design outputs *peak output power* and *continuous power dissipation* only apply to the sizing of linear amplifiers. Special attention is given to trapezoidal velocity profiles and piecewise-constant load force profiles, which are used in a companion spreadsheet [1]. Equations for power supply sizing and motor heating power are also provided.**

***Index Terms*—Amplifier Sizing, PWM Amplifier, Linear Amplifier, Linear Brushless Motor, Varedan Document 4083-42-005 Revision C**

I. Introduction

This document provides background and design equations for sizing linear and pulse-width modulated (PWM) amplifiers driving a three-phase linear brushless motor (LBM). The companion design spreadsheet [1] incorporates the design equations of Section IV below and provides an easy-to-use tool that will meet the needs of most designers. The analyses contained here can provide valuable context and help the designer understand limits of the design spreadsheet.

It is useful to think of amplifier sizing as a design process that incorporates design inputs and design outputs. For an LBM system design, the following design inputs are used to size an amplifier. SI units are used throughout unless otherwise noted.

- Motor force constant K_f (N/A_{rms})
- Motor phase-to-phase back-emf constant K_e $((V_{\phi\phi\,peak})/(m/sec))$
- Motor phase-to-phase resistance $R_{\phi\phi}$ (Ω)
- Motor phase-to-phase inductance $L_{\phi\phi}$ (H)
- Motor pitch p (m)
- Motor load mass m (kg)
- Worst-case velocity profile $v(t)$ (m/sec)
- Worst-case load force profile $F_{load}(t)$ (N)

The design methodology described herein produces design outputs that consist of five key amplifier requirements below. *Peak output power* and *continuous power dissipation* are design outputs that apply only to linear amplifier sizing as the specifications for output current in PWM amplifiers are sufficient to determine power dissipation – these are marked with an asterisk (*). The amplifier selection requirements are:

- Bipolar Amplifier Bus Voltage $\pm B$ for linear amplifiers (Amplifier Bus Voltage $2B$ for PWM amplifiers)
- Peak Output Current I_{peak}
- Continuous Output Current I_{cont}
- Peak Output Power* P_{peak}
- Continuous Power Dissipation* P_{cont}

Once the amplifier requirements are determined, a linear or PWM amplifier is sized with specifications that meet or exceed the requirements.

The remainder of this application note is organized as follows: In Section II, the design inputs are discussed and clearly defined. Algebraic equations that relate the design inputs to the design outputs are presented in Section III, and these equations are specialized to a trapezoidal velocity profile and a piecewise-constant load force profile in Section IV. The equations for a trapezoidal profile are implemented in the spreadsheet [1]. Section V contains some simple design checks. A numerical sizing example is given in Section VI, and some auxiliary equations for power supply sizing and motor ohmic heating are provided in Section VII. Section VIII makes some conclusions.

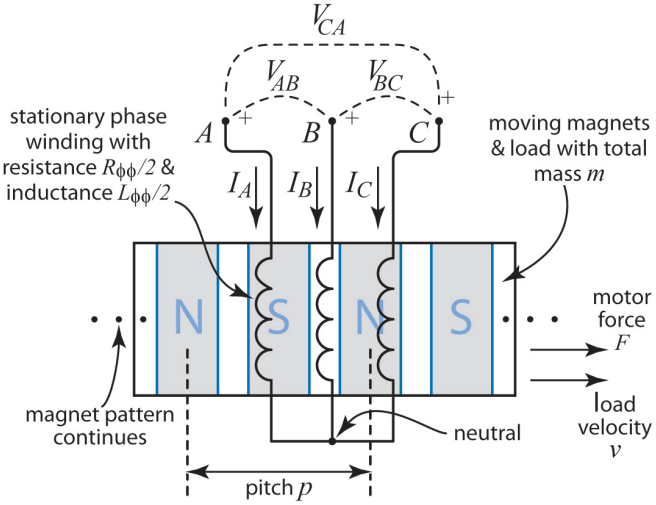

Fig. 1. Schematic diagram of a three-phase moving-magnet linear brushless motor.

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II. Background & Design Inputs

The notation describing a three-phase LBM is introduced in this section together with definitions of the design inputs used for amplifier sizing. Consider first the simplified schematic of a moving magnet LBM depicted in Fig. 1, which helps visualize the phase-to-phase voltages, the phase currents, and the load velocity. Moreover, two of many periods of the magnet array are shown, and the spatial period of the magnet array, referred to as the pitch p, is indicated. The spacing between the phase windings is $p/3$ and thus creates the symmetric electrical properties common to all three-phase LBMs.

Currents in phases A, B, and C produce forces on the moving magnets, and the motion of the magnets induce back-emf voltages in the phases. The motor force and back-emf constants are defined in terms of the motor currents, voltages, force, and velocity as follows.

A. Motor force constant K_f (N/A_{rms})

There are various definitions of motor force constant which capture, in rough terms, the ratio *(amount of motor force)* $\div$ *(amount of motor current)*. Up to a choice of units, the "amount of motor force" is clear. However, there are various ways to measure the currents in an LBM. There are three sinusoidal phase currents and each current can be measured with an root-mean-square (rms) current measurement or a peak current measurement. A common measurement of current in the three phases is the rms current in a single phase where *it is understood that the phases are driven with a sinusoidal current of that same magnitude and having phases separated by* $120°$. To indicate this convention, the current unit in the force-constant is labeled with rms as in N/A_{rms} (that all phases are driven is implied).

To be clear, consider the typical sinusoidal phase-current waveforms I_A, I_B, and I_C applied to an LBM are depicted in Figure 2.

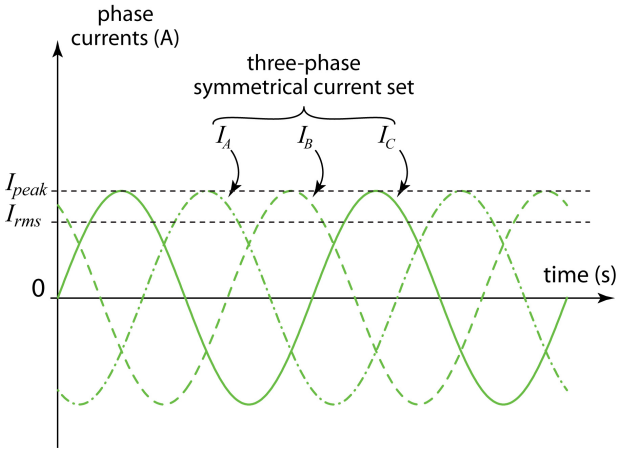

Fig. 2. Three-phase symmetrical current set used to drive an LBM. The figure depicts I_{rms}, which is used in the definition of K_f.

The current waveforms have an obvious symmetry and are referred to as a three-phase symmetrical current set. The three sinusoidal currents collectively produce a constant force F, and their frequency is determined by the motor velocity v and the commutation process. The rms current for each phase is the same and denoted by I_{rms}. The motor force constant with an rms current measurement is defined by

$$K_f \equiv F/I_{rms}, \tag{1}$$

where the symbol $\equiv$ is read "defined equal to." The force constant units are "force per amp rms" which is abbreviated $force/A_{rms}$. In the following sections, the SI force unit newton is used and K_f is expressed in N/A_{rms}.

Since the peak current is $\sqrt{2}$ times the rms current, a force constant expressed as $force/A_{peak}$ can be converted to $force/A_{rms}$ by multiplying the first number by $\sqrt{2}$ as summarized in Table I. The rms unit is used in the next sections and in the companion spreadsheet [1]. Again, in either unit definition, it is assumed that the three phases are driven with a symmetrical current set.

When computing the motor force F for evaluation in Equation 1, both inertial and load force terms are included. Let $a(t) \equiv \frac{dv}{dt}$ denote the acceleration and then one obtains

$$F = ma(t) + F_{load}. \tag{2}$$

The mass include mass reflected through any linkage or gearbox mechanism as described below.

B. Motor phase-to-phase back-emf constant K_e $(V_{\phi\phi\,peak}/(rad/s))$

An LBM acts as a generator, and when the magnet array is moved relative to the stationary phase windings voltages are produced across the phase windings. That is, the motor creates an electromotive force (emf), which is considered backward by common sign conventions and is thus called the back emf. If the "generator" phase terminals are disconnected or otherwise unloaded, the sinusoidal phase-to-phase voltages have the same amplitude and frequency.

In the sections below, the back emf voltage is measured phase-to-phase and its peak value is used. The units are subscripted with $\phi\phi$ to indicate "phase-to-phase" and with $peak$ to indicate that the peak value of voltage is used. Figure 3 shows $V_{\phi\phi\,peak}$ graphically. SI units are used herein so that velocity is expressed in m/sec.

Note that the frequency of the phase-to-phase voltage sinusoids, as are their amplitudes, are proportional to the motor velocity.

Unit	Multiply By	To Get
$force/A_{peak}$	$\sqrt{2}$	$force/A_{rms}$

TABLE I
Force Constant Conversion Factor

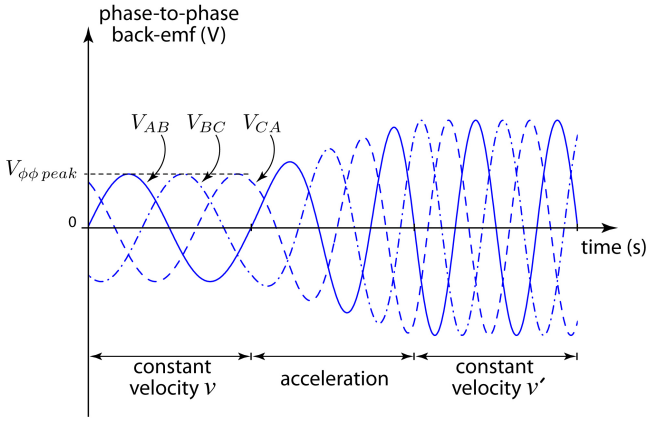

Fig. 3. Phase-to-phase voltage waveforms used in defining the back-emf constant K_e.

Since the back-emf voltage is proportional to velocity, and in reference to Figure 3, the back emf constant K_e is defined by

$$K_e \equiv V_{\phi\phi\,peak}/v. \tag{3}$$

The same K_e would be calculated at the higher velocity v' in Figure 3 with the proportionally higher peak voltage.

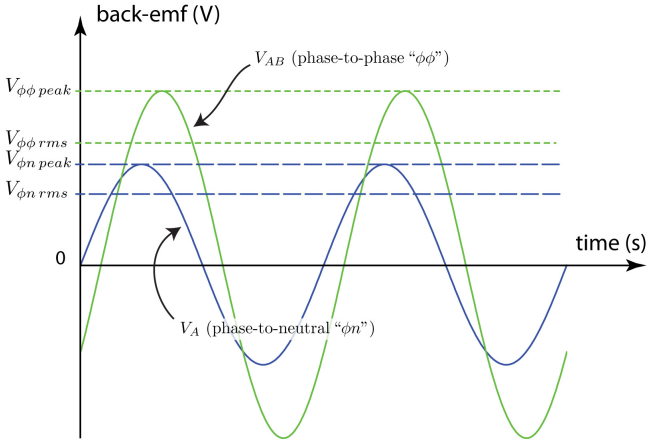

Fig. 4. Four ways to measure the back-emf voltage ($V_{\phi\phi\,peak}$, $V_{\phi\phi\,rms}$, $V_{\phi n\,peak}$, $V_{\phi n\,rms}$). $V_{\phi\phi\,peak}$ is the measure used in this application note and in the spreadsheet [1].

Unfortunately, there are four natural definitions for the back-emf constant that quantify the ratio *(amount of back-emf voltage) ÷ (motor speed)*. The definition based on phase-to-phase peak voltage is the one used in this application note and in [1], and there are three other definitions. In defining the back-emf voltage, there is a choice of where to measure a voltage (phase-to-phase or phase-to-neutral) and a choice between rms voltage or peak voltage. Thus, there are *four* natural measurements not to mention various units used for velocity (see Figure 4) and the design engineer must be clear on which measurement is used.

In reference to Figure 4, the peak voltage of a sinusoid is $\sqrt{2}$ times the rms voltage, and the amplitude of the phase-to-phase back-emf voltage $V_{\phi\phi}$ is $\sqrt{3}$ times the amplitude of the phase-to-neutral back-emf voltage $V_{\phi n}$. These ratios lead to the unit conversions of Table II where the speed unit is arbitrary, but constant, throughout the table.

C. Phase-to-phase resistance $R_{\phi\phi}$ (Ω) and inductance $L_{\phi\phi}$ (H)

Figure 1 shows the individual phase windings as having resistance $R_{\phi\phi}/2$ and inductance $L_{\phi\phi}/2$ such that the resistance and inductance measured across two phase terminals are $R_{\phi\phi}$ and $L_{\phi\phi}$ respectively.

D. Motor pitch p (m)

The motor pitch p is the spatial period of the magnet array in the motor. It is the distance between the north poles or, equivalently, the distance between the south poles.

E. Mass m (kg)

The mass m is the total moving mass internal and external to the motor. The internal mass is usually listed in the motor data sheet and the external mass includes mass reflected through any linkage or gearbox mechanism. As usual, any mass on the output of a linkage or gearbox is multiplied by the square of the speed ratio (output speed/input speed).

F. Worst-case velocity and load force profiles

A necessary design input for selecting an amplifier is one or more worst-case velocity profiles and load force profiles. The accelerations, loads, peak velocities, and other features of these profiles inform the amplifier selection. In choosing worst-case profiles, it is useful to understand how they affect the various design outputs. Further, such understanding can guide the system design and engineers may alter motion trajectories to reduce amplifier cost. A qualitative discussion of the velocity and load force profiles' impact on each of the design outputs is contained in subsections 1-5 below.

1) Peak Output Current: Short-time-constant thermal limits in connectors constrain the amplifier peak output current. Since large currents are needed to produce high forces, velocity profiles with high peak accelerations and load forces will place demanding requirements on the peak output current. Even a generally low-velocity low-acceleration profile can be challenging in terms of peak output current if there are short bursts of high inertial or load forces.

Unit	Multiply By	To Get
$V_{\phi\phi\,rms}/speed$	$\sqrt{2}$	$V_{\phi\phi\,peak}/speed$
$V_{\phi n\,peak}/speed$	$\sqrt{3}$	$V_{\phi\phi\,peak}/speed$
$V_{\phi n\,rms}/speed$	$\sqrt{6}$	$V_{\phi\phi\,peak}/speed$

TABLE II
BACK-EMF CONSTANT CONVERSION FACTORS

2) Continuous Output Current: Long time constants associated with conductor heating constrain the continuous output current. Trajectories that repeatedly accelerate and decelerate the load mass, or require high load forces, can cause overheating of conductors by exceeding the high continuous output current specification of an amplifier.

3) Peak Output Power:* As indicated by the asterisk, the peak output power calculated herein is used in sizing linear amplifiers only. In a linear amplifier, the peak output power is that peak power experienced by a single output transistor, or set of paralleled output transistors when acting as a single device. Heavy braking at high speeds causes large voltage drops and high currents in the output transistors, leading to high power dissipation. The peak output power dissipated in the transistors of a PWM amplifier is determined by the peak output current and a separate constraint on peak output power is redundant. High load forces with large output transistor voltage drops also cause high power dissipation in the output transistors.

4) Continuous Power Dissipation:* As indicated by the asterisk, the continuous power dissipation calculated herein applies to linear amplifiers only. Trajectories that repeatedly impose high inertial or load forces cause high continuous power dissipation in the output transistors of linear amplifiers. Over time, the temperature of the heat sink rises and the junction temperature of the power transistors can exceed specifications. The continuous power dissipation in the transistors of a PWM amplifier is determined by the continuous output current and a separate constraint on continuous power dissipation is redundant.

5) Bipolar Amplifier Bus Voltage for linear amplifiers, Amplifier Bus Voltage for PWM amplifiers: The bus-to-bus voltage in a linear or PWM amplifier must exceed the largest back-emf voltages, which are sinusoidal with zero mean. Thus, high-speed trajectories and/or motors with large back-emf constants can exceed the capabilities of the power supply/amplifier system. In linear amplifiers, the bus voltages are $+B\,Volts$ and $-B\,Volts$ and are referred to as the "bipolar amplifier bus voltage $\pm B$." In PWM amplifiers, the bus voltages are $0\,Volts$ and $2B\,Volts$ where the latter is normally referred to as the bus voltage and the bus at $0\,Volts$ is implied. The variable B is used in specifying both amplifier types in order to simplify notation. In either case, the bus-to-bus voltage is $2B$.

6) Trapezoidal Velocity Profiles: A common trapezoidal velocity profile will be used, in conjunction with the load force profile, to estimate the five key amplifier requirements. Trapezoidal profiles can be constructed to produce any of the demanding motions described in subsections 1-5 above and approximate many of the motions that a designer may generate. Figure 4 depicts an example of a trapezoidal profile with the timing and amplitude variables that precisely describe the motion. The analysis in Section IV refers to the profile of Figure 4.

*Calculations marked with an asterisk (*) only apply to linear amplifiers

The trapezoidal profile is assumed to be periodic with period T, which is 1.80 seconds for the profile in Figure 4.

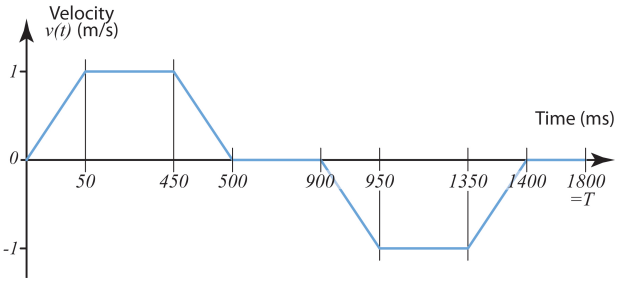

Fig. 5. Typical trapezoidal velocity design input.

7) Piecewise-Constant Load Force Profiles: In addition to inertial forces required to accelerate the mass m, the motor may also be used to apply, say, cutting forces in a machining operation. For the sake of amplifier sizing, the load force profile $F_{load}(t)$ is assumed to be piecewise constant and have transition times at the corner times of the velocity profile. See Figure 6 for an example of a piecewise-constant force profile. In referring to the constant force levels in the profile, we define $F_{load}(t+)$ to be the force immediately following time t. For example, in the profile in Figure 6, $F_{load}(50ms+) = 50N$, and $F_{load}(900ms+) = 0N$.

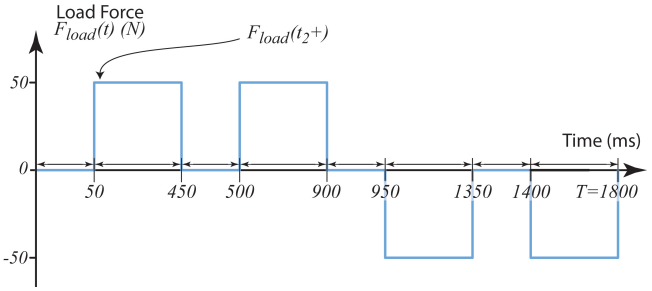

Fig. 6. Example of a worst-case piecewise-constant load force profile – a design input. $F_{load}(t_k+)$ denotes the force immediately following time t_k.

III. Design Outputs

In this section, the design outputs are expressed in terms of the design inputs. These equations serve as design tools for sizing the linear amplifier. Those outputs used only in linear amplifier sizing are marked with an asterisk (*).

A. Bipolar Amplifier Bus Voltage $\pm B$ for linear amplifiers, Amplifier Bus Voltage $2B$ for PWM amplifiers

The bipolar amplifier bus voltage $\pm B$ determines the limits of a linear amplifiers' output voltage range. The positive bus voltage is $+B$ or simply B, the negative bus voltage is $-B$, and the voltage between the buses is $2B$. For PWM amplifiers the bus voltages are 0 and $2B$ so that the voltage between the buses is also $2B$. The bus voltages must accommodate the phasor sum of the phase-to-phase back-emf voltage and the phase-to-phase voltage drops

for the phase resistance and inductance. More precisely, let $\max_t x(t)$ denote the maximum value of the function $x(t)$ for the allowable values of t. The peak phase-to-neutral voltage is given by

$$V_{\phi n\,peak} = \max_t \left[\left(\frac{\sqrt{2}(ma(t) + F_{load}(t))R_{\phi\phi}}{2K_f} + \frac{v(t)K_e}{\sqrt{3}} \right)^2 + \left(\frac{\sqrt{2}\pi(ma(t) + F_{load}(t))v(t)L_{\phi\phi}}{pK_f} \right)^2 \right]^{1/2}. \quad (4)$$

Since K_f is defined in terms of an rms current, a $\sqrt{2}$ appears in the expression above to obtain the needed peak current value. Similarly, K_e is a phase-to-phase back-emf constant, and a needed phase-to-neutral value is achieved by dividing by $\sqrt{3}$. Factors of 2 are used to convert the phase-to-phase $R_{\phi\phi}$ and $L_{\phi\phi}$ to phase-to-neutral values.

A margin of safety of, say, 20% is often included to account for variation in the line voltage and for other uncertainties. Thus, the bipolar amplifier bus voltage for a linear amplifier might be chosen to be

$$\pm B = \pm 1.2 V_{\phi n\,peak}. \quad (5)$$

The corresponding bus voltage for a PWM amplifier would be

$$V_{bus} = 2B = 2.4 V_{\phi n\,peak}. \quad (6)$$

B. Peak Output Current I_{peak}

The peak output current is defined as the maximum value of any phase current over the duration of a motion profile. The peak output current is given by

$$\begin{aligned} I_{peak} &\equiv \max_t \sqrt{2}|F(t)|/K_f \\ &= \max_t \sqrt{2}|ma(t) + F_{load}(t)|/K_f \end{aligned} \quad (7)$$

where $|\cdot|$ denotes absolute value and the expression in the second line of Equation 7 is in terms of the design inputs alone as desired.

C. Continuous Output Current I_{cont}

Long-time-constant thermal limits of conductors and other components affect the maximum continuous output current that an amplifier can supply. This specification is expressed as an rms value for one phase. Continuous operation above this threshold is expected to cause amplifier failures. For non-constant operation, the specification is interpreted as an rms current computed over a periodic velocity profile. That is, the "mean" in the root-mean-square current is taken over the period T of the motion:

$$I_{cont} = \left(\frac{1}{T} \int_0^T \left(\frac{ma(t) + F_{load}(t)}{K_f} \right)^2 dt \right)^{1/2}. \quad (8)$$

D. Peak Output Power* P_{peak}

For a linear amplifier, the peak output power is defined as the maximum instantaneous power dissipated in any single output transistor, or set of paralleled transistors acting as a single device, over the entire motion profile.

Linear amplifier output transistors have a limit on peak power dissipation, and this limit is approached when there is a large voltage drop across an output transistor while a high current is passing through that transistor. Such a high-voltage high-current condition arises when the motor velocity is large in magnitude, positive or negative, and the motor aggressively brakes. In this situation, the large back-emf and the bus voltages add constructively across the output transistor, and large currents are required. The phase-to-phase inductance $L_{\phi\phi}$ is neglected in the peak power calculation and, on occasion, this approximation should be reconsidered.

Express rms currents in terms of the force F and the force constant K_f, and express peak phase-to-phase back-emf voltages in terms of the velocity $v(t)$ and the back-emf constant K_e. Further, recall that $2B$ is the voltage across the power supply buses. Then the power dissipated in the active transistor in a linear amplifier at a current crest is computed to be

$$\sqrt{2}B|F|/K_f - R_{\phi\phi}F^2/K_f^2 - \sqrt{2}vFK_e/(K_f\sqrt{3}). \quad (9)$$

The $\sqrt{2}$ and $\sqrt{3}$ appear for the same reasons noted above. Note that the third term in the equation above increases the power dissipation if $v(t)$ and $F(t)$ have opposite signs as expected. Write $F(t) = ma(t) + F_{load}(t)$ and the expression for peak output power becomes, in terms of design inputs and the design output B,

$$\begin{aligned} P_{peak} = \max_t \Big[&\sqrt{2}B|ma(t) + F_{load}(t)|/K_f \\ &- R_{\phi\phi}(ma(t) + F_{load}(t))^2/K_f^2 \\ &- \sqrt{2}v(t)(ma(t) + F_{load}(t))K_e/(K_f\sqrt{3}) \Big]. \end{aligned} \quad (10)$$

For the simple trapezoidal velocity profile in Figure 5, the peak output power occurs at the beginning of the braking periods just following the $t = 450ms$ and $t = 1350ms$ points.

This peak output power calculation only applies to linear amplifiers.

1) Frequency Adjustments to Peak Output Power:* For short current pulses in linear amplifiers, the thermal mass of the transistor junction and surrounding silicon lowers the peak junction temperature relative to that for a DC current of the same magnitude. This beneficial frequency dependence

is quantified in the Transient Thermal Impedance for the transistor. For the devices used in Varedan amplifiers, the thermal mass effects are insignificant at commutation frequencies of about $5/3$ Hz and below. However, the effects are significant at 10Hz and above and can be exploited to achieve higher peak output power. Varedan does exploit the thermal mass effects when computing the safe operating area (SOA) of its linear amplifiers in order to avoid unnecessary fault conditions. Further, the thermal mass effects can be used in sizing amplifiers. Using the commutation frequency $f = v/p$, the junction-to-heat-sink thermal impedance for the MOSFET is conservatively approximated by

$$R_{j-HS}(f) = 10^{\left(0.08657 \ln\left(\frac{500}{f}\right) - 1.021\right)} + 0.05 \frac{^\circ C}{W}, \quad (11)$$

for $f \geq 5/3$. For $f < 5/3$, the impedance is constant and given by $R_{j-HS}(f) = R_{j-HS}(5/3)$. Thus, $5/3$ Hz is a corner in the frequency response estimate. Define the normalized response function as

$$n(f) \equiv R_{j-HS}(f)/R_{j-HS}(5/3), \quad (12)$$

which is equal to 1 for frequencies below $5/3$ Hz and is less than 1 for frequencies above $5/3$ Hz. The normalized response function $n(f)$ is used as a scale-factor to multiply the expression for P_{peak} in Equation (9) when comparing application requirements to the DC peak power specified for a given linear amplifier. Interestingly, this normalization can be used for all MOSFETs in the power range of Varedan's products. This is likely due to the thermal properties of silicon and similarities in the device geometries. This factor only applies to linear amplifiers and is not included in the standard spreadsheet [1].

E. Continuous Power Dissipation P_{cont}*

The continuous power dissipation in the output transistors of linear amplifiers has the potential to overheat the heat sink system and the transistor junctions. The average, over one commutation cycle, of the dissipation in the two output transistors of a single phase is given by

$$\frac{2\sqrt{2}}{\pi K_f}|F|B - \frac{R_{\phi\phi}F^2}{2K_f^2} - \frac{vFK_e}{\sqrt{6}K_f}. \quad (13)$$

Since the thermal time constant of the heat sink is long, the continuous power dissipation for a periodic velocity profile $v(t)$ is taken to be the average over the period T of the power dissipated in the three output stages. Using $F = ma + F_{load}$, one obtains

$$P_{cont} = \frac{3}{T}\int_0^T \left(\frac{2\sqrt{2}}{\pi K_f}|ma(t) + F_{load}(t)|B - \frac{R_{\phi\phi}(ma(t) + F_{load}(t))^2}{2K_f^2} - \frac{v(t)(ma(t) + F_{load}(t))K_e}{\sqrt{6}K_f} \right) dt. \quad (14)$$

The continuous power dissipation calculation only applies to linear amplifiers.

IV. Evaluating the Design Outputs for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles

Note that a trapezoidal velocity profile is equivalent to a piecewise-constant acceleration and load force profile. In this case, the design equations simplify and a systematic design process can be implemented in the companion spreadsheet [1]. Refer to the periodic trapezoidal velocity profile in Figure 5 and note that the profile can be specified by the nine corners in the trapezoidal profile that have (time, velocity) coordinates (0s, 0m/s), (50ms, 1m/s), (450ms, 1m/s), $\ldots$, (1.8s, 0m/s), or, for short, $(t_k, \omega(t_k))$; $k = 1, 2, \ldots, 9$. Similarly for $(t_k, F_{load}(t_k+))$; $k = 1, 2, \ldots, 9$, where $F_{load}(t_k+)$ denotes the load force just after the time t_k. Due to discontinuities at t_k, the value of the load force at t_k is not well defined. Again, the trajectory is assumed to be periodic with, in the case of Figures 5 and 6, a period of $T = 1.8s$.

The first observation to make is that the design outputs can be calculated using $v(t)$ and $F_{load}(t)$ near the corner points of the velocity profile. Specifically, the design outputs are determined by the velocities $v(t_k)$, and the acceleration $\alpha(t)$ just before and just after each corner, which are denoted $\alpha(t_k-)$ and $\alpha(t_k+)$ respectively. The forces $F_{load}(t_k+)$ and $F_{load}(t_k-)$ also enter the calculations and the inertial forces due to acceleration and load forces are combined into a single force. Since the trajectory is periodic, the acceleration just after the 9th corner is the same as that just after the 1st corner. That is $\alpha(t_9+) = \alpha(t_1+)$ - similarly $F(t_9+) = F(t_1+)$. The acceleration and load forces are often discontinuous right at the corners and not defined there – although inductance in the motor windings and other sources of filtering will round the corners in practice.

When restricted to trapezoidal velocity and piecewise constant load force profiles, the five design equations above are simplified as follows.

A. Bus Voltages for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles

The peak output voltage amongst all the profile corners is obtained by maximizing over all values of the corner velocities $v(t)$, the accelerations $a(t_k\pm)$ just before and just after to the corners, and $F(t_k\pm)$. Thus, the calculation

involves $8 \times 2 = 16$ different evaluations of the peak phase-to-neutral voltage given by the expression in square brackets below.

$$V_{\phi n\, peak} = \max_{k,\pm} \left[\left(\frac{\sqrt{2}(ma(t_k\pm) + F_{load}(t_k\pm))R_{\phi\phi}}{2K_f} + \frac{v(t_k)K_e}{\sqrt{3}} \right)^2 + \left(\frac{\sqrt{2}\pi(ma(t_k\pm) + F_{load}(t_k\pm))v(t_k)L_{\phi\phi}}{pK_f} \right)^2 \right]^{1/2} \quad (15)$$

When choosing the "+" or "-" in the maximization, one or the other is used in place of $\pm$ in right-hand side of the equation.

Applying a recommended margin of 20% yields

$$\pm B = \pm 1.2 V_{\phi n\, peak} \quad (16)$$

for the bipolar amplifier bus voltage for a linear amplifier. The bus voltage for a PWM amplifier is

$$V_{bus} = 2B = 2.4 V_{\phi n\, peak}. \quad (17)$$

B. Peak Output Current for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles

The relationship for the peak output current becomes

$$I_{peak} = \max_{k,\pm} \sqrt{2}|ma(t_k\pm) + F_{load}(t_k\pm)|/K_f. \quad (18)$$

C. Continuous Output Current for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles

The acceleration is constant between corner times in a trapezoidal velocity profile. Thus, the integral expression for the continuous output current becomes the summation

$$I_{cont} = \left(\frac{1}{T} \sum_{k=1}^{8} \left(\frac{ma(t_k+) + F_{load}(t_k+)}{K_f} \right)^2 (t_{k+1} - t_k) \right)^{1/2}, \quad (19)$$

where $T = t_9$ is the period of the velocity profile.

D. Peak Output Power for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles*

The peak output power in a linear amplifier occurs just before or just following one of the corners so that the expression for P_{peak} becomes:

$$P_{peak} = \max_{k,\pm}\ n \left(\frac{v(t_k)}{p} \right) \times \left[\frac{\sqrt{2}B|ma(t_k\pm) + F_{load}(t_k\pm)|}{K_f} - \frac{R_{\phi\phi}(ma(t_k\pm) + F_{load}(t_k\pm))^2}{K_f^2} - \frac{\sqrt{2}v(t_k)(ma(t_k\pm) + F_{load}(t_k\pm))K_e}{K_f\sqrt{3}} \right], \quad (20)$$

where either the "+" or the "-" is used in place of the $\pm$ symbol throughout. The normalized response function (not used in the spreadsheet [1]) is used to incorporate the benefit of the thermal mass of the transistor junctions. The peak output power calculation only applies to linear amplifiers.

E. Continuous Power Dissipation for Trapezoidal Velocity Profiles and Piecewise-Constant Load Force Profiles*

The acceleration and load forces are constant on intervals for the given profiles. Thus, integrating the expression for continuous power dissipation in a linear amplifier for such profiles yields

$$P_{cont} = \frac{3}{T} \sum_{k=1}^{8} \left(\frac{2\sqrt{2}}{\pi K_f}|ma(t_k+) + F_{load}(t_k+)|B - \frac{R_{\phi\phi}(ma(t_k+) + F_{load}(t_k+))^2}{2K_f^2} - \frac{(v(t_k) + v(t_{k+1}))(ma(t_k+) + F_{load}(t_k+))K_e}{2\sqrt{6}K_f} \right) \Delta t_k, \quad (21)$$

where $\Delta t_k = (t_{k+1} - t_k)$. This calculation does not apply to PWM amplifiers.

V. Design Checks

In implementing the design equations above, there are various ways that errors may occur. In addition to the usual transcription and algebraic errors, confusion may arise from the various definitions of force and back-emf constants, as well as from the unit systems used. There are a couple simple checks that are worth performing.

A. $\frac{K_f}{K_e} = \sqrt{\frac{3}{2}} \approx 1.22$

For the definitions and SI units used herein the above relationship between the force and back-emf constants holds for ideal three-phase motors. Since magnetic materials exhibit nonlinearity, and the back-emf waveforms can deviate from the ideal sinusoids because of motor geometry, variations of up to a few percent might be observed in some motor data sheets. However, if there is confusion in units, or confusion in the use of rms vs. peak or phase-to-phase vs. phase-to-neutral in parameter definitions, checking the ratio $\frac{K_f}{K_e}$ will reveal the error.

B. $\dfrac{L_{\phi\phi}}{R_{\phi\phi}} = \tau_e$

Motor data sheets are usually redundant in quoting resistance, inductance, and motor electrical time constant τ_e. The relationship above holds up to round-off and measurement error. If millihenries are used instead of henries and milliseconds are used instead of seconds in the calculations, errors will escape detection with this check.

C. $\dfrac{L_{\phi\phi}}{R_{\phi\phi}} \ll \min_{k=1,\ldots,8}(t_{k+1} - t_k)$

For good tracking, the electrical time constant of the motor should be much shorter than any of the time periods defining the trapezoidal velocity profile. This is rarely a problem, but it is easy to check.

VI. Sizing Example

To illustrate the use of the design equations, consider an amplifier sizing process to address the following design inputs:

- Motor force constant $K_f = 39\,N/A_{rms}$
- Motor phase-to-phase back-emf constant $K_e = 32\,V_{\phi\phi\,peak}/(m/s)$
- Motor phase-to-phase resistance $R_{\phi\phi} = 2.7\,\Omega$
- Motor phase-to-phase inductance $L_{\phi\phi} = 18\,mH$
- Motor pitch $p = 24\,mm$
- Motor load mass $M = 24.6\,kg$
- Worst-case velocity profile $v(t)$ given by Figure 5.
- Load force profile equal to zero: $F_{load}(t) = 0$.

A. Example: Amplifier Bus Voltage

The peak output voltage occurs when acceleration and velocity are both high. This peak occurs in the profile of Figure 5 just prior to corner two and just prior to corner six when the peak voltage reaches the same value. Thus, using the formula for $V_{\phi n\,peak}$ and setting the load force to zero, one has:

$$V_{\phi n\,peak} = \left[\left(\frac{\sqrt{2}ma(t_2-)R_{\phi\phi}}{2K_f} + \frac{v(t_2)K_e}{\sqrt{3}}\right)^2 + \left(\frac{\sqrt{2}\pi ma(t_2-)v(t_2)L_{\phi\phi}}{pK_f}\right)^2\right]^{1/2} \quad (22)$$

From Figure 5, the velocity at corner two is $1m/s$ and the acceleration just prior to corner two is $\dfrac{\frac{1m}{s}}{0.050s} = 20m/s^2$. Thus,

$$V_{\phi n\,peak} = \left[\left(\sqrt{2}\cdot 24.6\cdot 20\cdot 2.7/(2\cdot 39) + 1\cdot 32/\sqrt{3}\right)^2 + \left(\pi\cdot\sqrt{2}\cdot 24.6\cdot 20\cdot 1\cdot 0.018/(0.024\cdot 39)\right)^2\right]^{1/2} = 59.8\,V \quad (23)$$

Applying a recommended margin of 20% yields, for a linear amplifier,

$$\pm B = \pm 71.8\,V. \quad (24)$$

For a PWM amplifier, the bus voltage is

$$V_{bus} = 2B = 143.6\,V. \quad (25)$$

.

B. Example: Peak Output Current

In the example, the peak current occurs at peak acceleration, which is equal to $20m/s^2$ for the profile of Figure 5. This current peak occurs during all of the velocity ramps. In particular, the peak occurs just following corner one, such that

$$I_{peak} = \frac{\sqrt{2}m|a(t_1+)|}{K_f} = \sqrt{2}\cdot 24.6\cdot\frac{20}{39} = 17.8\,A \quad (26)$$

C. Example: Continuous Output Current

The rms current has the same value of $17.8\,A/\sqrt{2}$ during the four 50ms ($0.2s$ total) velocity ramps and $0\,A$ during periods of constant velocity. The continuous current is therefore

$$I_{cont} = \left(\frac{1}{T}\sum_{k=1}^{8}\left(\frac{ma(t_k+)}{K_f}\right)^2(t_{k+1}-t_k)\right)^{1/2} = \left(\frac{1}{1.8}\left(\frac{24.6\cdot 20}{39}\right)^2(0.2)\right)^{1/2} = 4.21\,A \quad (27)$$

D. Example: Peak Output Power*

For this example and linear amplifiers, the peak output power occurs just after corners three and seven, at which point the power reaches the same high level. First, the commutation frequency is computed as

$$f = \frac{v(t_3)}{p} = \frac{1}{0.024} = 41.7\ \text{Hz}. \quad (28)$$

Using this value of f, which is greater than the corner frequency of $5/3$, one obtains

$$R_{j-HS}(41.7) = 10^{\left(0.08657\ln\left(\frac{500}{41.7}\right)-1.021\right)} + 0.05 = 0.168°C/W, \quad (29)$$

and

$$n(f) \equiv \frac{R_{j-HS}(7.23)}{R_{j-HS}(5/3)} = 0.816. \quad (30)$$

The peak frequency-adjusted power is then computed as

*Calculations marked with an asterisk (*) only apply to linear amplifiers.

$$P_{peak} = n(\frac{v(t_3)}{2\pi p}) \left[\frac{\sqrt{2}Bm|a(t_3+)|}{K_f} - \frac{R_{\phi\phi}(ma(t_3+))^2}{K_f^2} - \frac{\sqrt{2}mv(t_3)a(t_3+)K_e}{K_f\sqrt{3}} \right] \quad (31)$$

$$= 0.816 \cdot \left[\frac{\sqrt{2} \cdot 71.8 \cdot 24.6 \cdot 20}{39} - \frac{2.7 \cdot 24.6^2 \cdot (-20)^2}{39^2} - \frac{\sqrt{2} \cdot 24.6 \cdot 1 \cdot (-20) \cdot 32}{(39 \cdot \sqrt{3})} \right] = 963\,W \quad (32)$$

This calculation applies only to linear amplifiers.

E. Example: Continuous Power Dissipation*

Finally, the continuous power dissipation formula with zero load force is

$$P_{cont} = \frac{3}{T} \sum_{k=1}^{8} \left(\frac{2m\sqrt{2}}{\pi K_f}|a(t_k+)|B - \frac{R_{\phi\phi}m^2a(t_k+)^2}{2K_f^2} - \frac{(v(t_k)+v(t_{k+1}))ma(t_k+)K_e}{2\sqrt{6}K_f} \right) \Delta t_k, \quad (33)$$

where $\Delta t_k = t_{k+1} - t_k$. Equation (33) can be simplified by observing that power is dissipated during acceleration on the velocity ramps only. Further, there are two kinds of ramps. There is one case in which acceleration and velocity have the same sign and another case in which they have the opposite signs. Thus, the continuous power dissipation can be calculated by computing just these two cases and doubling the result (the first factor of 2 in the equation below). The two cases are distinguished by different signs prior to the terms in square braces below (these terms cancel):

$$P_{cont} = 2 \cdot \frac{3}{1.8} \left\{ \left(\frac{2 \cdot 24.6\sqrt{2}}{\pi \cdot 39} \cdot 20 \cdot 71.8 - \frac{2.7 \cdot 24.6^2 \cdot 20^2}{2 \cdot 39^2} - \left[\frac{\sqrt{2} \cdot 24.6 \cdot (0.5) \cdot 20 \cdot 32}{2\sqrt{3} \cdot 39} \right] \right) (0.050) + \left(\frac{2 \cdot 24.6\sqrt{2}}{\pi \cdot 39} \cdot 20 \cdot 51.2 - \frac{2.7 \cdot 24.6^2 \cdot 20^2}{2 \cdot 39^2} + \left[\frac{\sqrt{2} \cdot 24.6 \cdot (0.5) \cdot 20 \cdot 32}{2\sqrt{3} \cdot 39} \right] \right) (0.050) \right\} = 200\,W \quad (34)$$

This calculation applies to linear amplifiers only.

VII. Power Supply Sizing & Motor Ohmic Heating

The primary objective of this application note is to provide requirements for choosing an amplifier. It is possible, in addition, to compute power supply requirements and motor heat dissipation requirements, since these additional requirements can be determined from the same design inputs used in the amplifier sizing equations.

A. Power Supply Sizing

For a linear amplifier, the averaged absolute value of a phase current over one commutation cycle is the peak current divided by $\pi/2$. The worst-case power over one commutation cycle into each phase amplifier is then $2BI_{peak}/\pi$, and for all three phases one has $6BI_{peak}/\pi$. Since the power supply is bipolar for linear amplifiers, the power per bus is half the total or

$$P_{bus\,linear} = 3BI_{peak}/\pi. \quad (35)$$

The current per bus is

$$I_{bus\,linear} = 3I_{peak}/\pi. \quad (36)$$

For a PWM amplifier, the power delivered on the single bus of voltage $2B$ is the same as the power delivered by the two buses of voltage $\pm B$ to a linear amplifier. Thus,

$$P_{bus\,PWM} = 6BI_{peak}/\pi. \quad (37)$$

Dividing by the bus voltage $2B$ for a PWM amplifier, one finds that the current requirement for a PWM amplifier is the same as that for a linear amplifier:

$$I_{bus\,PWM} = 3I_{peak}/\pi. \quad (38)$$

B. Motor Ohmic Heating $P_{motor-heat}$

The power input to the motor is converted to power output in the form of either mechanical power in the motor shaft or heat that must be transferred from the motor to the ambient environment. The dominant source of heat generated in the motor is the I^2R losses in the three windings, which are given by

$$P_{motor-heat} = \frac{3}{2}I_{cont}^2R_{\phi\phi}. \quad (39)$$

The division by 2 converts the phase-to-phase resistance $R_{\phi\phi}$ to the phase-to-neutral value. There are other sources of heat generation in a motor associated with eddy-currents, windage, friction, and hysteresis losses in motor. While these sources are generally negligible in a conservative thermal design, they can be included by adding power losses associated with viscous and Coulomb friction losses listed in the data sheet. Eddy-currents contribute to viscous losses, and hysteresis is usually lumped with friction measurements.

VIII. Conclusion

The equations of Section IV provide a simple means to size linear and PWM amplifiers for driving LBMs. As noted above, the *peak output power* and *continuous power dissipation* calculations apply only to the sizing of linear amplifiers, which is indicated with an asterisk (*) throughout. In many applications, S-curves rather than lines define accelerations. The rounding of the corners by using such S-curves reduces the demand on the amplifiers. If one is attempting a close fit of the motion requirement to the amplifier specification, a more complete simulation (e.g. in Matlab) will provide more information. In the case of S-curves, the equations of Section III can be used. Such a simulation can easily incorporate some of the inductance modeling omitted herein. Finally, care should be taken with long-period motions and aperiodic motions, as an assumption was made that the thermal time-constant ($\sim$60 seconds) of the heat sink systems is long enough that the heat sink temperature is constant and determined by P_{cont}.

Questions and feedback on this application note are most welcome and can be directed to Varedan via email at sales@varedan.com.

References

[1] "Spreadsheet: Sizing Linear and PWM Amplifiers Driving a Linear Brushless Motor," Varedan Technologies Document 4083-42-001.

[2] Hurley Gill, "Servomotor Parameters and their Proper Conversions for Servo Drive Utilization and Comparison," Kollmorgen Inc.